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Transparent, Flexible Silicon Nanostructured Wire Networks with Seamless Junctions for High Performance Photodetector Applications

Mozakkar Hossain[‡], Gundam Sandeep Kumar[#], Barimar Prabhava S.N^{#, ‡}, Emmet D. Sheerin[#], David McCloskey[#], Somobrata Acharya^{||}, K.D.M. Rao^{‡}, and John J. Boland[#]*

[‡]Technical Research Centre, Indian Association for the Cultivation of Science, Jadavpur, Kolkata-700032, India.

[#]School of Chemistry, Centre for Research on Adaptive Nanostructures and Nanodevices (CRANN), Trinity College Dublin, Dublin, Ireland.

^{||}Centre for Advanced Materials, Indian Association for the Cultivation of Science, Jadavpur, Kolkata-700032, India.

* To whom any correspondence should be addressed,

Email: trckdmr@iacs.res.in, mallik2arjun@gmail.com

[‡]These authors contributed equally to the paper

KEYWORDS: Transparent, Flexible, Silicon nanostructured wire network, Photodetector, Porous Silicon.

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3 **ABSTRACT:** Optically transparent photodetectors are crucial in next generation
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5 optoelectronic applications including smart windows and transparent image sensors. Designing
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7 photodetectors with high transparency, photoresponsivity and robust mechanical flexibility
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9 remains a significant challenge, as is managing the inevitable tradeoff between high
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11 transparency and strong photoresponse. Here we report a scalable method to produce flexible
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13 crystalline Si nanostructured wire (NW) networks fabricated from silicon-on-insulator (SOI)
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15 with seamless junctions and highly responsive porous Si segments that combine to deliver
16
17 exceptional performance. These networks show high transparency (~ 92% at 550 nm),
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19 broadband photodetection (350 nm to 950 nm) with excellent responsivity (25 A/W), optical
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21 response time (0.58 ms) and mechanical flexibility (1000 cycles). Temperature dependent
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23 photocurrent measurements indicate the presence of localized electronic states in the porous Si
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25 segments, which play a crucial role in light harvesting and photo-carrier generation. The
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27 scalable low-cost approach based on SOI has the potential to deliver new classes of flexible
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29 optoelectronic devices, including next generation photodetectors and solar cells.
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37 Transparent electronics is a promising technology for producing devices that are
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39 transparent to visible light while delivering full device functionality,¹⁻³ and has the potential to
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41 impact a wide range of applications such as solar cells,⁴ light emitting diodes,⁵ and Li-ion
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43 batteries,⁶ sensors⁷ and photodetectors.⁸ Transparent photodetectors, in particular, would enable
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45 new applications in smart windows, optical communications, video imaging, security and
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47 human motion detection.⁹ The addition of flexibility further expands the range of applications
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49 to next generation optoelectronic devices¹⁰ and holds the potential to meet the growing
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51 demands for light-weight compatibility, large-area scalability and adaptability to curved
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53 surfaces.¹¹
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57 Commercially available photodetectors are produced with crystalline silicon (Si),
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59 silicon carbide (SiC), InGaAs *etc.*, which are rigid, brittle, expensive to fabricate and
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3 completely opaque.¹² Importantly, it is desirable to design transparent and flexible
4 photodetectors with broad range spectral response that operate at room temperature.¹³⁻¹⁴ The
5 development of transparent photodetectors with excellent mechanical flexibility and high
6 photoresponse over a broad spectral range remains a challenge.¹⁰ Traditional thin-film based
7 metal oxide transparent photodetectors have limited photoresponse and are constrained to rigid
8 substrates.¹⁵⁻²⁰ Recently, there have been several attempts to fabricate transparent
9 photodetectors using low-dimensional nanomaterials by exploiting their optical, electrical and
10 mechanical properties.^{2,8,9,21-27} Two-dimensional (2D) materials such as graphene², WSe₂²⁵ and
11 WS₂²⁶ exhibit remarkably high photoresponse, but however, possess poor light absorption
12 cross-sections and short carrier lifetimes. On the other hand, 1D nanowire (NW) based
13 photodetectors have shown significant potential with high photosensitivity and ultra-fast
14 response.^{8-9, 21-24} Amongst the 1D materials studied to date, Si NWs have shown the most
15 promise due to their well-controlled electrical and optical properties along with superior
16 photoresponse performances.^{14,28,29} High photoresponse from individual crystalline Si NW
17 connected by short porous silicon segments has also been demonstrated.³⁰ Photoresponse of a
18 single Si NW based photodetector was shown to increase with decreasing wire diameter.³¹
19 However, devices based on individual NWs involve tedious lithography processes and limited
20 to applications with small active areas.

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45 Recently, percolative NW network based transparent photodetectors have received
46 much attention.^{8,9,21-24} Aligned SnO₂ NW based transparent photodetectors exhibited a good
47 photoresponse but limited transparency.⁹ Random percolative NW networks formed with
48 SnO₂-ZnO showed good transparency but the photoresponse is restricted to a narrow spectral
49 range.²³ Moreover, high contact resistance between individual NWs limits the photocurrent and
50 response time of these devices.^{8,9} To date, there has been no attempt to improve the contact
51 resistance of these percolative NW networks. Here, we undertake the challenge of fabricating
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3 semiconducting NW networks with minimal contact resistance and high uniformity. We
4 demonstrate the fabrication of transparent photodetectors using single-crystalline percolative
5 Si NW networks with seamless junctions. The Si NW networks are successfully transferred
6 onto a PET (polyethylene terephthalate) substrate to achieve 92% transparency at 550 nm.
7 Photodetectors fabricated from these Si NW networks showed exceptional responsivity (25
8 A/W) over a wide spectral range from 350 nm to 950 nm, fast response times of 0.58 ms, in
9 addition to excellent flexibility, making them suited for high performance transparent
10 photodetector applications.
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21 **RESULTS AND DISCUSSION**

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23 The process for fabricating the Si NW network is based on crackle lithography in combination
24 with Si dry etching (Figure 1). In the first step, acrylic resin nanoparticle dispersion is spin-
25 coated on a silicon-on-insulator (SOI) substrate (Figure 1a) to produce the crackle template.³²
26 This is a simple, low-cost and scalable approach to forming uniform and highly interconnected
27 nanostructured wire networks. In the second step, metal (Au/Ti) deposition and lift-off results
28 in the formation of an Au NW network (see Figure 1b, c) that serves as an etch mask for
29 patterning silicon in an inductive coupled plasma (ICP) etching process (see Figure 1d). The
30 etching parameters are optimized to achieve Si NW network with flat edges over the entire 2
31 cm² area of the device. The patterned Si NW network is then transferred on to a PET substrate
32 by etching the underlying SiO₂ layer of the SOI in HF solution (Figure 1e). In the final step,
33 the metal over the Si NW network is etched to yield the Si NW network (Figure 1f). Randomly
34 distributed localized regions of nanoporous Si are created within the crystalline Si NWs
35 network using metal-assisted chemical etching (see Methods).³³ All the steps involved in this
36 fabrication process are convenient, scalable and extensively used in the silicon industry.
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55 The optical micrograph in Figure 1g shows the fabricated Si NWs are interconnected
56 across the entire network with a uniform fill factor of 12%. The average width of a Si NW is
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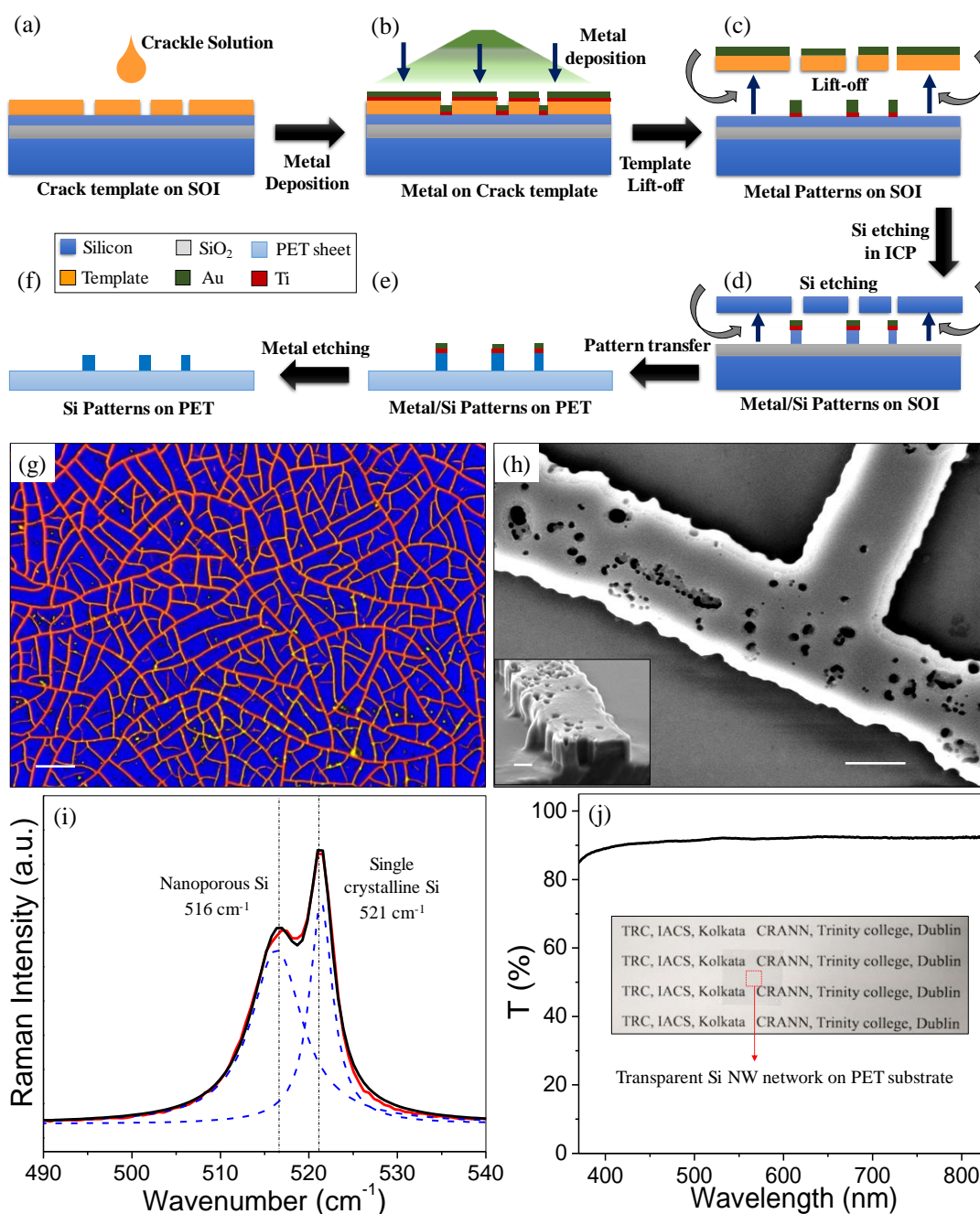


Figure 1. Schematic illustration of Si NW network fabrication (a) crackle template on SOI substrate, (b) deposition of the metal on template, (c) template lift-off, (d) after Si etching in ICP, (e) transferred to PET and (f) etching metals (Au, Ti), (g) Optical microscope image of a Si NW network on PET substrate (False color, scale bar 50 μm), (h) scanning electron microscope image of a Si NW network junction (600 nm scale bar) and inset shows cross sectional view of a Si NW (250 nm scale bar). (i) Raman spectra (red line) of Si NW network (blue dotted lines, black lines indicate deconvoluted peaks). (j) Transmittance spectrum of Si NW network, inset is a photograph of Si NW network on PET substrate.

870 ± 240 nm and average distance between the NWs is ~ 30 μm (Supporting Information, Figure S1). Scanning electron microscopy (SEM) images reveal the seamless junctions between Si NWs (Figure 1h) which are crucial for efficient carrier transport across the

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3 network.³² Cross sectional view of Si NW indicates the presence of steep edges (inset, Figure
4 1h). Interestingly, we observe the presence of nanopores with sizes ranging from 30 nm to 200
5 nm throughout the Si NW network (Supporting Information, Figure S2 and S3). The formation
6 of the porous nanostructure is facilitated by using Au nanoparticles that are known to assist in
7 chemical etching of Si substrates.³³ The porous nanostructure of Si NW network is crucial for
8 achieving high performance photodetectors.³⁰ Raman spectrum of Si NW network shows peaks
9 at 521 cm^{-1} and 516 cm^{-1} corresponding to single-crystalline Si and porous Si, respectively
10 (Figure 1i).³⁴ TEM image of Si NW clearly demonstrates the porous nature, while the selected-
11 area electron diffraction reveals the single-crystalline nature of the wires (Supporting
12 Information, Figure S4). These observations demonstrate that the network is comprised of a
13 random distribution of nanopores embedded within a Si single crystal framework. The
14 transmission spectrum of the Si NW network shows uniform transmittance over the entire
15 visible spectrum, displaying a transmittance of 92% at 550 nm (Figure 1j). This is higher than
16 the expected transmittance based on the measured 12% fill factor, and attributed to the
17 diffraction of light around the wire segments of Si NW network. Absorption of Si NW network
18 in the UV region is found to increase gradually due to quantum confinement effects of the
19 nanopores (Supporting Information, Figure S5).³⁵

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22 We have studied the performance of the Si NW network as a photodetector by depositing an
23 array of Au electrodes using a shadow mask with 40 μm gap between electrodes (Supporting
24 Information, Figure S6). The photoresponse characteristics of the device were measured with
25 white light and monochromatic light of various wavelengths (800 nm, 700 nm, 600 nm and
26 500 nm). The current-voltage (I-V) characteristics indicate a pronounced photoresponse upon
27 white light illumination in comparison to the dark current (Figure 2a). The photocurrent
28 demonstrates a linear dependence with the illumination intensity and the device is not saturated
29 in the intensity range used (Supporting Information, Figure S7).³⁶

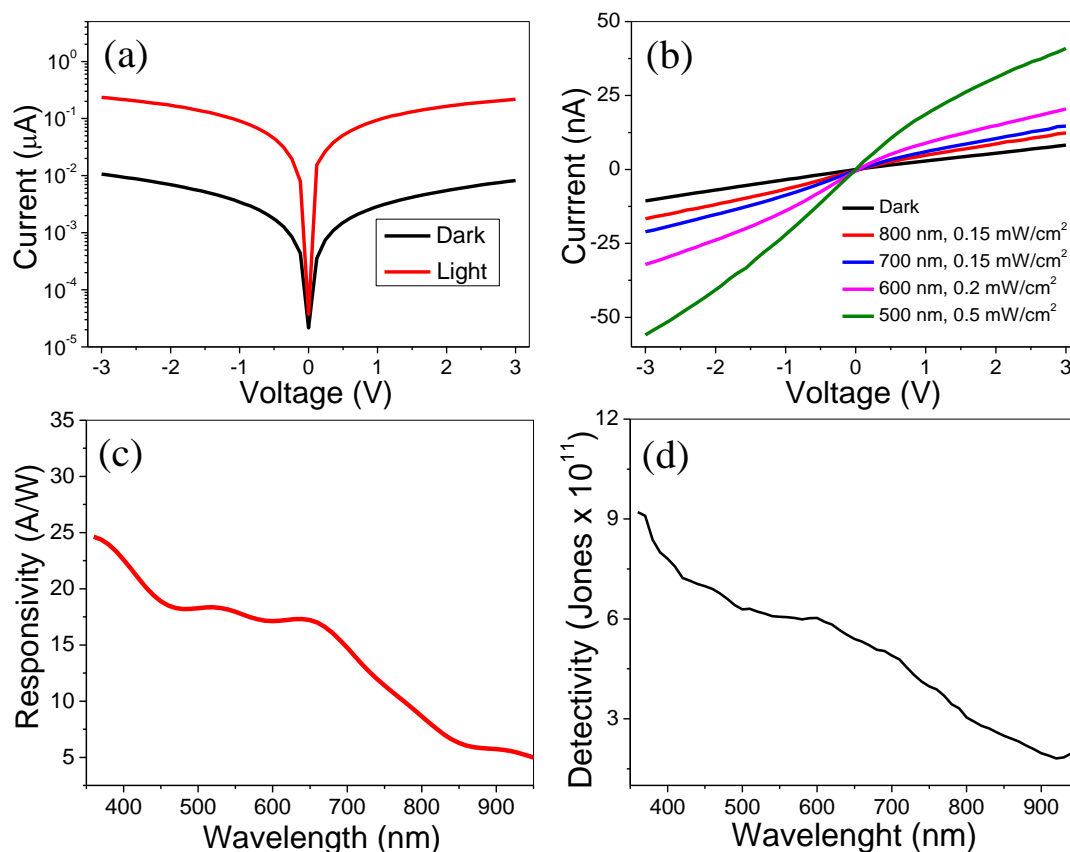


Figure 2. Current-voltage curves of Si NW network photodetector illuminated with, (a) white light (semi-logarithmic scale) of intensity 100 mW/cm^2 , (b) monochromatic light of various wavelengths 500 nm, 600 nm, 700 nm and 800 nm and corresponding intensities are 0.5 mW/cm^2 , 0.2 mW/cm^2 , 0.15 mW/cm^2 and 0.15 mW/cm^2 , respectively and in dark, (c) responsivity and (d) detectivity as a function of wavelength under an applied bias of 2 V.

A gradual increase in photocurrent is observed with decreasing wavelength from 800 nm to 500 nm (Figure 2b). The photosensitivity, which is defined as $(I_{\text{light}} - I_{\text{dark}})/I_{\text{dark}}$, is $\sim 2000\%$ under white light illumination of 100 mW/cm^2 for Si NW network with pores.³⁷ In contrast, a Si NW network without porosity shows a photosensitivity of $\sim 300\%$ (Supporting Information, Figure S8). The control experiment clearly demonstrates the importance of porosity in Si NW network for achieving high performance photodetectors, consistent with a previous report.³⁰ We have also tested traditional photodetector figure of merits such as responsivity and detectivity (Figure 2c,d).^{13,38} The spectral responsivity R_{λ} is defined as $R_{\lambda} = J_{\text{ph}}/P_{\text{light}}$, where $J_{\text{ph}} = I_{\text{ph}}/A$ is the photocurrent density; I_{ph} , P_{light} and A are the photocurrent, input light intensity and the effective area of the device respectively.^{38,39} In our device, the effective area is

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3 considered to be 12% of total area of the device where the Si NW network is present. The
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5 spectral responsivity value is measured to be 24.6 - 4.9 A/W over the wavelength ranging from
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7 350 nm to 950 nm (Figure 2c).
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10 Another important parameter of a photodetector is detectivity, which is defined as $D =$
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12 $R_{\lambda}/(2qJ_d)^{1/2}$ (Supporting Information, Section 1) where R_{λ} is spectral responsivity, where q and
13
14 J_d are the charge of the electron and dark current density, respectively.^{29,39,40} The detectivity is
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16 found to be 9×10^{11} Jones (Figure 2d), which is comparable to commercially available Si-
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18 based non-transparent photodetectors (4×10^{12} Jones) and is higher than ZnO based transparent
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20 photodetectors.⁴¹ The external quantum efficiency is measured to be 9000% (Supporting
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22 Information, Figure S9) for our transparent Si NW network photodetectors, which is
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24 comparable with other opaque photodetectors based on Mo-doped ReSe₂ nanosheets⁴², MoO₃
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26 nanosheets⁴³, and Nb₂O₅ nanoplates.⁴⁴ The device performance of Si NW network is notable
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28 compared to previously reported transparent detectors based on WSe₂²⁵, graphene², ZnO⁴¹ and
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30 WO₃¹⁶. We have made a comprehensive survey of literature-reported on transparent
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32 photodetectors and compared the performances with our results (Supporting Information, table
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34 S1).^{8,9,15-27} Our Si NW network photodetector exhibits the best spectral range, the maximum
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36 transparency and one of the best responsivity and detectivity values reported to date. The
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38 outstanding device performance is attributed to the porous Si structure and presence of
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40 seamless junctions across the Si NW network. As described below, the porous Si serves as a
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42 reservoir of charge carriers due to the presence of localized electronic states that release charge
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44 carriers under light illumination, whereas the seamless junctions ensure efficient transport of
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46 the carriers across the entire network due to the alignment of its bands relative to those in
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48 surrounding silicon.⁴⁵⁻⁴⁷
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56 Response time is another important parameter for a photodetector which describes the
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58 ability of a device to switch in response to high speed optical signals. Transient photocurrent
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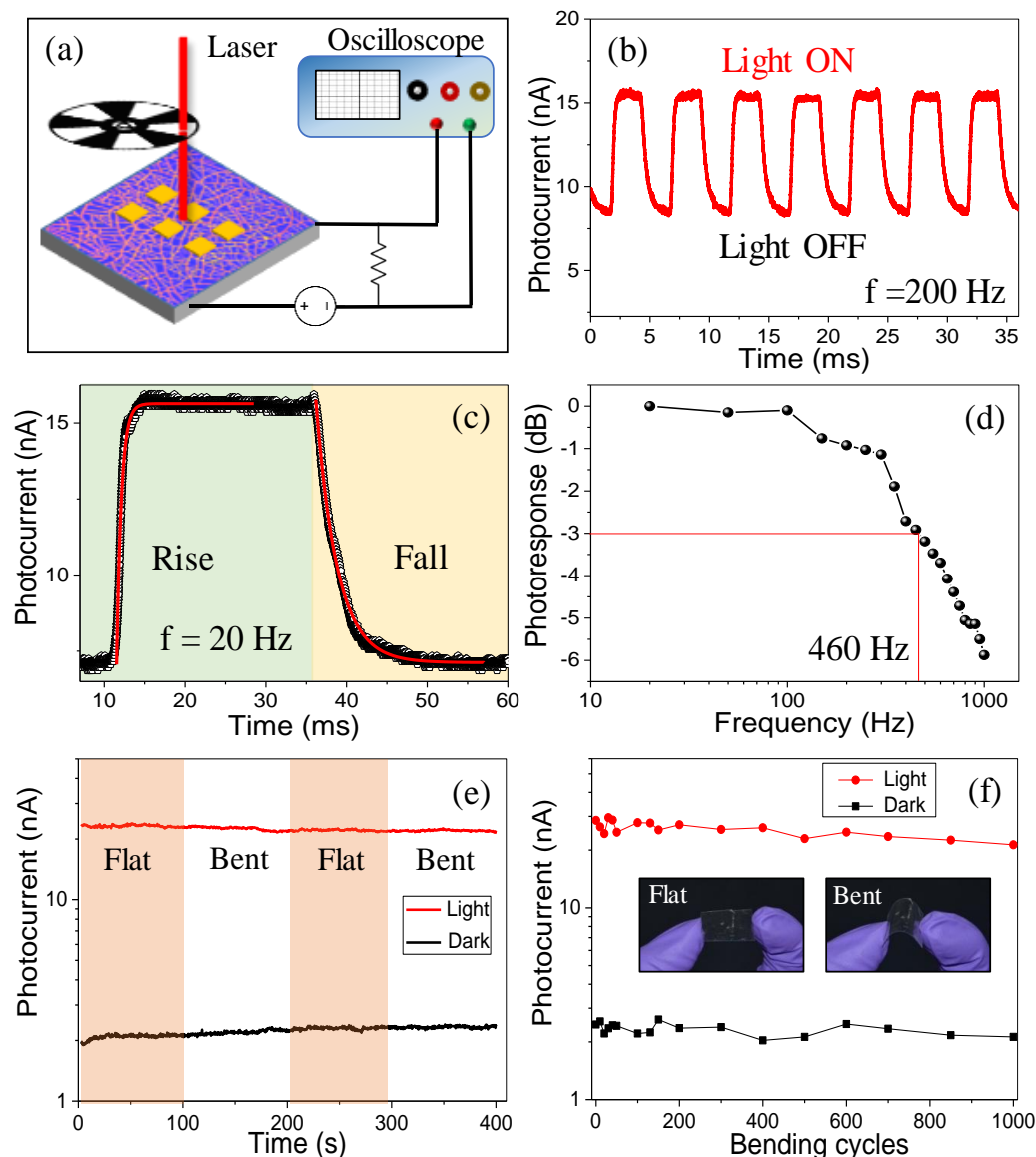


Figure 3. Transient photocurrent of the Si NW network at 650 nm light illumination under 2 V bias modulated by optical chopper with frequency, (a) schematic of the experimental setup, (b) transient photocurrent at 200 Hz and (c) rise time and fall time estimated at 20 Hz, black dots and red curves represent experimental data and exponential fits, respectively, (d) normalized photoresponse as a function of modulation signal frequency and corresponding cutoff frequency (f_{-3dB}) is 460 Hz, marked with red line. Time-dependent photocurrent plots for the flexible Si NW network photodetector (e) while it is in bent and flat positions, (f) the photocurrent and dark current after different bending cycles (1000). The inset shows the digital photographs of the device in flat and bent position.

measurements of Si NW networks were performed with red laser (laser power ~ 3 mW, 650 nm) to study the stability and repeatability of the photodetector using an optical chopper setup as shown in the Figure 3a. The transient photoresponse of the device under the dark and light illumination is measured by applying a bias of 2 V. Figure 3b illustrates the photocurrent of

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3 the Si NW network under modulated light of frequency 200 Hz. It is interesting to visualize the
4 rapid increase and decrease in the photocurrent when the device is exposed to light (light ON)
5 and dark (light OFF) respectively. The durability of the device was further tested by performing
6 continuous ON/OFF switching for 1000 cycles at 500 Hz (Supporting Information, Figure
7 S10c). The photocurrent characteristics reveal a stable, rapid photoresponse and reversible
8 photo switching over multiple cycles. The photoresponse measurements at low frequency (20
9 Hz) as shown in Figure 3c, illustrate the saturation current in ON state and constant current in
10 OFF state. Rise time and fall time are estimated using the following exponential equations,
11 respectively (Supporting Information, Section 2).⁴⁸
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$$I = I_0 - I_0 \times e^{\left(\frac{-x}{t_r}\right)} \dots\dots\dots (1)$$

$$I = I_0 + A_1 \times e^{\left(\frac{-x}{t_f}\right)} \dots\dots\dots (2)$$

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30 For the representative single cycle shown in Figure 3c the rise and fall times are estimated to
31 be 0.58 ms and 1.82 ms, respectively. This response time is the best reported value in literature
32 for transparent photodetectors (Supporting Information, Table S1), which is attributed to the
33 seamless junctions of NW in Si NW network. Figure 3d illustrates the normalized
34 photoresponse as a function of modulation frequency from 20 Hz to 1 kHz for the Si NW
35 network. The photoresponse is near constant in the low frequency region 20-100 Hz, consistent
36 with the saturation of photocurrent (see Figure 3b, 3c and S10).^{39,49} The frequency at which
37 photoresponse becomes half of its initial value is called the cut-off frequency (f_{-3dB}), which is
38 estimated to be 460 Hz.^{38,39} These modulation frequency measurements indicate a faster
39 performance compared to hybrid ZnO nanowire networks.^{8,23} However our Si NW
40 photodetector is slower than conventional silicon diodes, due to the carrier life time of trap
41 states in the porous Si.
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57 Flexibility and environment durability are crucial for practical applications of
58 photodetectors.¹⁰ The flexibility of the Si NW network photodetector was monitored by flexing
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3 the device up to a 5 mm bending diameter (Supporting Information, Figure S11). The
4 photocurrent and dark current measured across the 40 μm device is shown in Figure 3e. The
5 photoresponse remains unchanged in the flat and bent configurations, demonstrating the
6 flexibility of the device. The minimal reduction in current on bending is attributed to the
7 seamless junctions within the network and contrasts with the behavior in deposited NW
8 networks where junction sliding is known to occur during deformation.⁵⁰ Figure 3f records the
9 photocurrent and dark current of the device over the course of 1000 bending cycles. The
10 photoresponse is effectively unchanged indicating excellent robustness, in addition to
11 flexibility.

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13 To investigate the mechanism of electrical transport through the Si NW network, we
14 performed temperature dependent studies in dark as well as under illumination. Figure 4a
15 shows the dark resistivity of Si NW network over the temperature range 100K to 325K, where
16 the resistivity increases monotonically as the temperature is lowered. This behavior is
17 consistent with 3D Mott variable range hopping (VRH) and the resistivity is well described by
18 the following equation.⁵¹

$$\rho(T) = \rho_0(T) \exp[(T_0/T)^{1/4}] \dots\dots\dots (3)$$

19 where ρ is resistivity, T is temperature, ρ_0 is characteristic resistivity and T_0 is characteristic
20 temperature. We estimate the density of states near Fermi energy to be $N(E_F) = 10^{19} \text{ eV}^{-1} \cdot \text{cm}^{-3}$
21 using fitting parameters from equation 3 (Supporting Information, Section 3), which is
22 comparable with polycrystalline Si.^{46,52} 3D Mott VRH has previously been reported in
23 vanadium oxide nanowire networks.⁵³ The enhanced DOS near Fermi energy ($N(E_F)$) indicates
24 the existence of localized shallow trap states near the band edge.⁵² The VRH model suggests
25 that the conduction in porous Si NW network arises due to the hopping of charge carriers
26 through localized trap states.⁵¹ The photocurrent through Si NW network gradually increases
27 with increasing intensity of light illumination (Figure 4b), although, the rate of increase of
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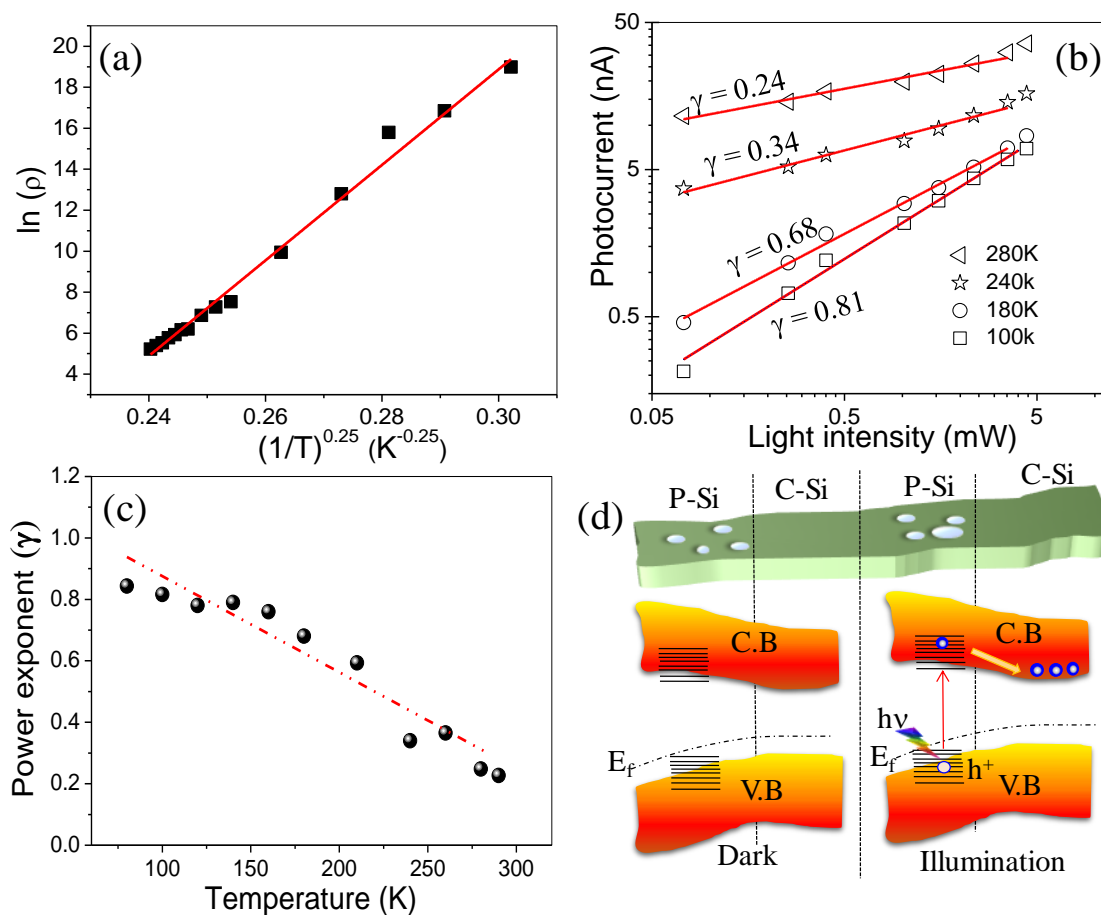


Figure 4. (a) Temperature dependence of resistivity with respect to $T^{-0.25}$ where $T = 100\text{K}$ to 325K , red line is the linear fit for data. (b) Dependence of photocurrent with respect to white light intensity on the Si NW network under 2 V bias at various temperatures, red line is the power law fit ($I_{ph} \propto P^\gamma$) for data. (c) Temperature dependence of power exponent γ under white light illumination. (d) Schematic representation of Si NWs with porosity, valence band and conduction band in dark and light illumination.

photocurrent is temperature dependent. The photocurrent (I_{ph}) scales with the light intensity (P) following a power law ($I_{ph} \propto P^\gamma$), where γ varies from 0.24 to 0.81 (Figure 4b).⁵⁴ This photoresponse is ascribed to induced photocurrent from extended trap states within the band gap of the porous Si.⁵⁵ The dependence on the light intensity approached the expected linear behavior at low temperature (at 100K, $\gamma = 0.81$) nevertheless it is sub-linear at high temperatures (at 280K, $\gamma = 0.24$). The change from a sub-linear to linear response is gradual (see Figure 4c), and consistent with electronic doping by localized trap states.⁵⁵ A similar photoresponse was observed in Si^{31} and GaN^{54} NWs.

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3 Based on the above observations and performance of the device, we now speculate on
4 the physical mechanism behind the photoresponse of our porous Si NW networks. Figure 4d
5 depicts a schematic energy band diagram of the Si NW network with porous Si (P-Si) and
6 crystalline Si (C-Si) segments in dark and under illumination. The band gap of P-Si is larger
7 than that of C-Si due to the quantum confinement leading to enhanced absorption of UV light
8 (Figure S5).⁴⁵ The surface defects of P-Si segments results in the formation of localized trap
9 states as described in VRH model (Figure 4d).⁵¹ In the dark, under applied bias the injected
10 charge carriers are trapped in localized states, leading to low current. In contrast, the enhanced
11 roughness of P-Si segments leads to reduced reflection and a stronger light interaction that
12 allows charge carriers to be excited to the conduction band. The absorption of the UV light
13 insures that charge carriers can be excited from broad range of localized trap states compared
14 to lower energy visible-IR light, consistent with the high photoresponse in the UV region
15 shown in Figure 2c.³⁰ The excited charge carriers from P-Si segments are swept into the C-Si
16 segments due to their relative band alignment. The charge carriers can then easily reach the
17 electrodes through neighbouring C-Si, facilitated by the seamless network junctions, resulting
18 in the observed enhanced photoresponse and device performance.

40 **Conclusions:**

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42 In summary, we have demonstrated the fabrication of Si NW networks with seamless
43 junctions. The facile fabrication method results in the generation of localized P-Si regions
44 while retaining the single-crystallinity of Si NW network, and was confirmed by SEM, TEM
45 and Raman spectroscopy measurements. The Si NW network photodetector demonstrated a
46 high transparency of 92% at 550 nm with an excellent response over a wide spectral range.
47 Low-temperature and power law dependency measurements showed that the device
48 performance originates from a reservoir of localized electronic states in the P-Si regions.
49 Reliable and rapid photo switching characteristics with a response time of 0.58 ms was
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3 observed and attributed to the seamless nature of the junctions of Si NW network. Mechanical
4 flexibility measurements showed the photoresponse performance was unaffected even after
5 1000 bending cycles. These devices are among the best performing transparent and flexible
6 photodetectors ever reported. More importantly, the fabrication method developed here can be
7 applied to thin films and 2D materials, with the potential to meet the demands of next
8 generation optoelectronic devices.
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16 **Methods:**

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19 **Fabrication of Si NW network:** The commercially available SOI (silicon-on-insulator) with
20 a 260 nm silicon (100) device layer with resistivity of 1-5 ohm.cm and 2 μm SiO₂ (SOITEC)
21 wafers were used and patterned accordingly. The substrates are cleaned sequentially with
22 water, acetone and isopropyl alcohol in ultrasonic bath for 10 minutes. Commercially available
23 acrylic resin (Ming Ni Cosmetics Co., Guangzhou, China) solution was dispersed in water with
24 a loading of ~ 0.2 to 2 mg/ml in deionized water. The dispersion was stirred for 10 minutes,
25 and followed by ultrasonication for 30 minutes. The resultant dispersion was centrifuged at ~
26 3000 rpm for 5 minutes to remove the large and un-dissolved particles of the acrylic resin. As
27 prepared crackle solution was spin coated at 1000-2000 rpm for 2 minutes. Further, multi-
28 layers of metals Ti (10 nm), and Au (50 nm to 100 nm) were sequentially deposited on the
29 crackle template by thermal evaporation process (Hind High Vacuum Co., India). Sacrificial
30 crackle templates were lifted off using the chloroform solution for 5-6 minutes. Inductive
31 coupled plasma (ICP) etching process (OIPT Plasma Lab System100 ICP180, Oxford
32 Instruments) with SF₆ and CHF₃ gases was employed to etch the exposed Si layer in between
33 the metal wire network. In the next step, photoresist is patterned in the form of 50 μm holes
34 separated by 100 μm pitch using optical lithography setup (OAL mask aligner). The photoresist
35 patterns facilitate the transfer process from SOI substrate to PET sheet. The substrate with
36 Si/Ti/Au NW network along with photo resist patterns was dipped in a diluted hydrofluoric
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3 (HF) acid solution for 3 minutes to etch the SiO₂ (2 μm) layer. Subsequent dipping of substrate
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5 in water/ethanol (3:1) solution stimulates the Si NW network layer to float on top of the solution
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7 surface which is transferred onto a PET sheet (Supporting Information, Figure S12). The
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9 photoresist is removed in acetone and metal etching is performed for 1-5 minutes using Au
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11 etchant (KI:I₂:H₂O in 2:1:5 ratio) and Ti etchant (NH₄OH:H₂O₂ in 1:2 ratio), respectively. The
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13 porous Si is obtained by dipping Si NW network in HF:H₂O₂:H₂O (2:1:5 ratio) solution along
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15 with Au nanoparticles for 1-5 minutes (Supporting Information, Section 4).³³ The resultant
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17 PET substrates were annealed on hotplate at 80 °C for 5 minutes.
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21 **Fabrication of Si NW network based photodetectors:** The substrates with Si NW network
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23 is dipped in 10% HF solution for 0.5 to 1 min to etch SiO₂ (surface oxide) on top of Si.
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25 Immediately, Au thin film electrodes were deposited using shadow mask with 40 μm gap
26
27 electrodes. Before doing the electrical characterization, the devices were annealed at 80 °C for
28
29 10 min.
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33 **Photoconductivity measurements:** The current–voltage characteristic measurements were
34
35 carried out with the help of TTPx Lakeshore probe-station connected to a Keithley 4200
36
37 semiconductor characterization system (4200 SCS). The low temperature measurements were
38
39 performed at ~ 10⁻⁵ mbar vacuum. For photoconductivity measurements, Newport Solar
40
41 Simulator (model:66902) and Optem Schott light source were used. The wavelength dependent
42
43 photocurrent measurements were performed using Newport Solar Simulator connected with
44
45 monochromator. The frequency response measurements were conducted with a red laser diode
46
47 (LD-RL-6-5v, ~ 3 mW, 650 nm), Thor labs optical chopper (MC2000B-EC) and oscilloscope
48
49 (scientific instruments). The flexibility tests were performed using a home-built bending
50
51 apparatus in tandem with probe station (Supporting Information, Figure S11).
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55 **Characterization techniques:** Raman spectroscopy (Raman Triple spectrometer Jobin-Yvon
56
57 T64000) measurements were carried out by exciting the sample with Nd:YAG green laser
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3 (532.5 nm, ~ 2 mW power). Morphology of the silicon network was visualized using FESEM
4
5 a Nova NanoSEM 600 instrument (FEI Co., The Netherlands). Transmission electron
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7 microscope (TEM) and selected area electron diffraction (SAED) patterns were measured using
8
9 an ultrahigh resolution field emission gun transmission electron microscope (UHRFEG-TEM),
10
11 JEOL, JEM 2100 F field emission electron microscope. Transmission measurements were
12
13 performed using the Varian Cary 5000 UV-Vis-NIR spectrophotometer.
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16 17 **ASSOCIATED CONTENT**

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20 The supporting information is available free of charge on the ACS Publications website.
21
22 Further information on the Si NW wire network fabricated in this study, including diameter
23
24 and pore size distribution statistics, UV-Vis spectra, SEM, TEM and Optical microscopy
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26 imaging, external quantum efficiency. A comprehensive survey of literature on transparent
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28 photodetectors Table S1 is also included. The experimental details regarding Si NW network,
29
30 including transferring on PET sheet, porosification process and flexibility measurement set-up.
31
32 The authors declare no competing financial interest.
33
34
35

36 37 **AUTHOR INFORMATION**

38 39 **Corresponding Author**

40
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42
43 ** Email: trckdmr@iacs.res.in, mallik2arjun@gmail.com
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45

46 47 **Author Contribution**

48
49 M.H., G.S.K., B.P.S. and K.D.M. designed and performed the experiments; E.D.S., and D.M.,
50
51 performed the transient photoresponse experiments; M.H., G.S.K. and B.P.S. analyzed the
52
53 data. M.H., G.S.K., B.P.S., S.A., J.B. and K.D.M. wrote the manuscript; K.D.M. performed
54
55 overall guidance of the project. All authors read and approved the manuscript. M.H., G.S.K.
56
57 and B.P.S. contributed equally.
58
59
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Graphical Table of Content:

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31 Transparent, flexible Silicon nanostructured network for high performance broadband
32 photodetectors

